

7/21/06

10/72259

(FILE 'HOME' ENTERED AT 16:17:02 ON 21 JUL 2006)

FILE 'CAPLUS' ENTERED AT 16:17:21 ON 21 JUL 2006

L1 744553 SEA ABB=ON PLU=ON SEMICONDUCTOR OR SEMI CONDUCTOR OR CHIP OR
IC OR INTEGRATED CIRCUIT OR MICROCHIP OR MICRO CHIP OR
WAFER#####

L2 35117 SEA ABB=ON PLU=ON CIRCUIT#####(3A)(LAYER##### OR
COAT##### OR FILM##### OR COVER##### OR OVERLAY####
OR COVERCOAT##### OR STACK##### OR LAMIN#####)

L3 274013 SEA ABB=ON PLU=ON (HEAT##### OR THERM##### OR TEMP#####)(3A
) (LAYER##### OR COAT##### OR FILM##### OR COVER####
OR OVERLAY##### OR COVERCOAT##### OR STACK#####
OR LAMIN#####)

L4 1724966 SEA ABB=ON PLU=ON SILIC##### OR SI OR SILIC#####
ALLOY#####

L5 779572 SEA ABB=ON PLU=ON PASSAG##### OR DUCT OR ORIFIC#####
OR CANAL OR CHANNEL OR GROOV##### OR OUTLET OR PORT OR
CAVIT#####

L6 6953 SEA ABB=ON PLU=ON RESISTOR(3A)(FILM##### OR COAT##### OR
LAYER##### OR COVER#####)

L7 464869 SEA ABB=ON PLU=ON (REDUC##### OR DECREAS##### OR
PREVENT##### OR ELIMINAT##### OR LOWER##### OR
MINIM##### OR SUPPRES#####)(5A)(HEAT##### OR
HOT SPOT OR TEMP#####)

L8 27955 SEA ABB=ON PLU=ON (FLUID##### OR WATER)(3A)(EVAPORAT#####
OR VIBRAT##### OR BUMP#####)

L9 0 SEA ABB=ON PLU=ON L1 AND L2 AND L3 AND L4 AND L5 AND L6 AND
L7 AND L8

L10 1251 SEA ABB=ON PLU=ON L1 AND L2 AND L3

L11 40 SEA ABB=ON PLU=ON L10 AND L6

L12 4 SEA ABB=ON PLU=ON L11 AND L7

L13 1 SEA ABB=ON PLU=ON L10 AND L8

L14 644 SEA ABB=ON PLU=ON L1 AND L2 AND L3 AND L4

L15 38 SEA ABB=ON PLU=ON L14 AND L5

L16 3 SEA ABB=ON PLU=ON L15 AND L7

L17 0 SEA ABB=ON PLU=ON L15 AND L8

L18 15 SEA ABB=ON PLU=ON L1 AND L3 AND L8

L19 15 DUP REM L18 (0 DUPLICATES REMOVED)

L20 3 SEA ABB=ON PLU=ON L18 AND L5
D L12 1-4 IBIB ABS

L21 1 SEA ABB=ON PLU=ON L13 NOT L12
D L21 IBIB ABS

L22 3 SEA ABB=ON PLU=ON L16 NOT (L12 OR L13)
D L22 1-3 IBIB ABS

L23 3 SEA ABB=ON PLU=ON L20 NOT (L12 OR L13 OR L16)
D L23 1-3 IBIB ABS

L24 11 SEA ABB=ON PLU=ON L18 NOT (L12 OR L13 OR L16 OR L20)
D L24 1-11 IBIB ABS

FILE 'STNGUIDE' ENTERED AT 16:40:09 ON 21 JUL 2006